

FIG. 1A

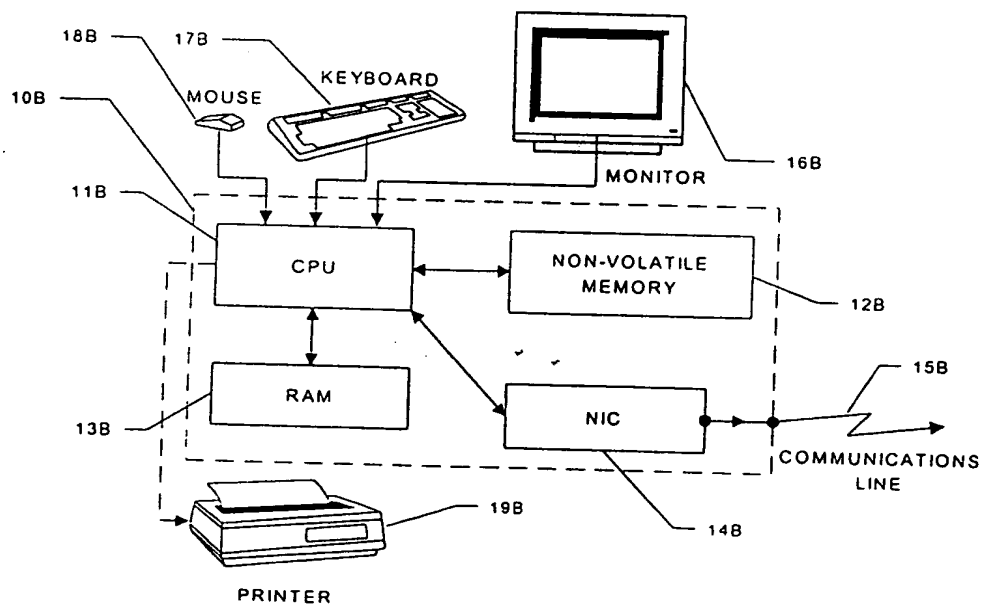
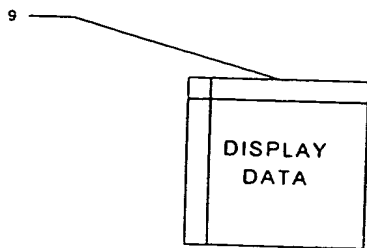


FIG. 1B

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Insert memory mappings for volatile and non-volatile memory.

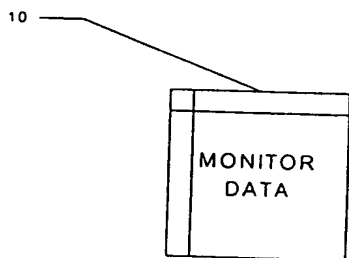


FIG. 1C

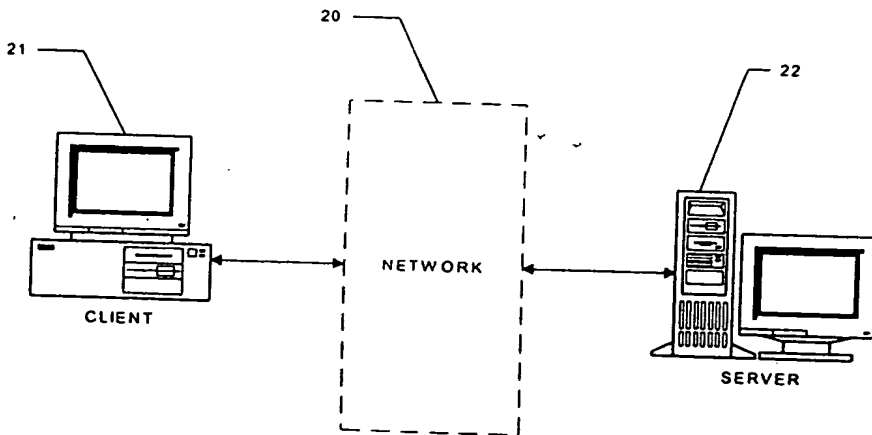


FIG. 1D

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